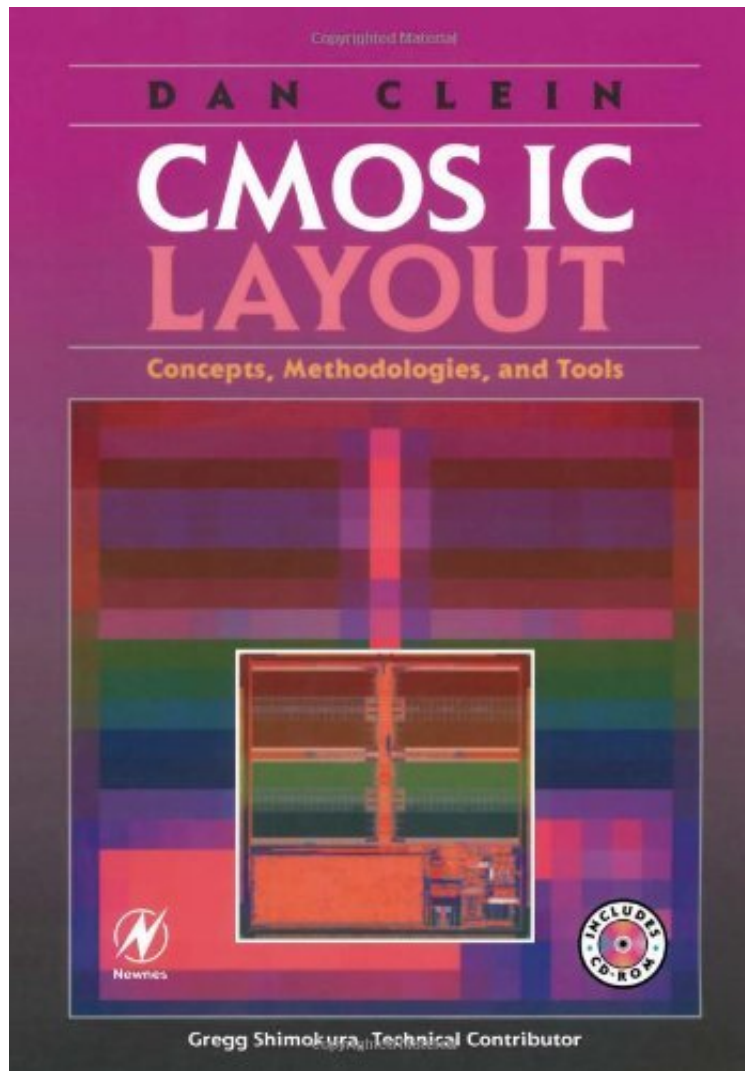


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CMOS IC Layout: Concepts, Methodologies, and Tools

Von Dan Klein

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Von Dan Klein : CMOS IC Layout: Concepts, Methodologies, and Tools before purchasing it in order to gage whether or not it would be worth my time, and all praised CMOS IC Layout: Concepts, Methodologies, and Tools:

KundenrezensionenHilfreichste Kundenrezensionen0 von 0 Kunden fanden die folgende Rezension hilfreich. authors reviewVon Dan KleinMost textbooks on VLSI cover circuit design techniques or algorithms for CAD tools but none of them are explaining extensively IC Layout challenges. For today's industry, with so many new tools, platforms, methodologies, and design styles being developed all the time, a more unified format is needed. Now here it is, and is for more readers that you may think:Students studying VLSI Design need a comprehensive textbook for IC Layout so their projects can be actually manufactured... Students studying Computer Engineering or Science and tools for VLSI

need a book to help them understand the physical limitations from processing and design... IC layout designers around the world need a detailed training curriculum that covers more than just one type of layout... Design engineers need a book that can help them to understand the layout concepts so their designs can be more realistic and easier to implement... Software developers need a source of information beyond "clean room" specifications, they need a users voice with the specific industry language... Technical marketing and sales people need a dictionary to understand the language and the concepts required by their customers when they sale software and their own design teams when they sell a product...But a book about layout is not complete if there are no color graphics. The problem is that colored pages inside a black and white book is very expensive and would have raised the price of our book. We wanted to make this book as affordable as possible so together with the publishers we decided to offer you a solution, a CD-ROM with additional color pictures. So, you will find in the CD-ROM attached all the color files that we prepared for your edification. But this is not all; we included a full demo version of L-Edit, from Tanner Research Inc. The demo content includes the entire Tanner tools demo so is useful for a new designer or student to try each of them. From our point of view is a tremendous opportunity for a reader to try as he/she learns about each topic. The demo provides access to "danclein" library so once you learned how to open the library all our secrets will be in your hands. In the near future, we will try to build a web site that can be used as questions and answer place. Please send us Email at cometic@ieee.org for any enquiry. We hope that you will enjoy reading our book at list as much as we enjoyed writing it!

Dan Clein Gregg Shimokura 0 von 0 Kunden fanden die folgende Rezension hilfreich. CMOS IC Layout book review Von Ein Kunde I have been a layout designer for 10 years and this is the first book I have found that is devoted exclusively to ic layout. It is about time. Your book does a good job of covering layout in the broad sense which make sit good for beginners and junior layout designers. I especially think you did a good job explaining the concepts of layers, basic design rules and transistor fingering. I do feel that you could have spent more time with transistor, resistor and capacitor matching from an analog perspective. Like most text and tools, your emphasis was on digital circuit. Keep up the good work and I look forward to the 2nd edition... 0 von 0 Kunden fanden die folgende Rezension hilfreich. Fairchild ttl Data book Cmos Ic (RAM and memory) Von Ein Kunde I just want some more latest product reference book. I have old one and I want to upgrade and see the data of new product. Could you guide me to select the best one for me. I appreciate to your cooperation and heartily guidance for me. From: Ed (Saudi Telecom co.ltd, K.S.A.)

Kurzbeschreibung This book includes basic methodologies, review of basic electrical rules and how they apply, design rules, IC planning, detailed checklists for design review, specific layout design flows, specialized block design, interconnect design, and also additional information on design limitations due to production requirements. *Practical, hands-on approach to CMOS layout theory and design* Offers engineers and technicians the training materials they need to stay current in circuit design technology. *Covers manufacturing processes and their effect on layout and design decisions

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